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Information
Disclosure
Statement
O'Callahan
1/3/95

PATENT

Docket No. 29284/327

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors : Takao WATANABE et al.
Serial No. : (Unassigned)
Filed : (Herewith)
For : SEMICONDUCTOR DEVGICE INCOR-
PORATING INTERNAL POWER SUPPLY
FOR COMPENSATING FOR DEVIATION ON
OPERATING CONDITION AND FABRICA-
TION PROCESS CONDITIONS
Group Art Unit : 2511
Examiner : (Unassigned)

HON. COMMISSIONER OF PATENTS
AND TRADEMARKS
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97 & § 1.98

S I R:

In conformance with Applicants' duty of disclosure under 37 C.F.R. § 1.56 (a) and § 1.97(b)(1), references listed on the attached form PTO-1449 are hereby brought to the Examiner's attention.

Since all of the references were either cited by the Examiner in the parent application or submitted therein by applicants, no copies of the references are submitted herewith.

It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record

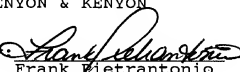
therein and appear in the "references cited" on any patent to issue therefrom.

Respectfully submitted,

KENYON & KENYON

Date: 12 September 1994

By:


Frank Pietrantonio
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